

ON Semiconductor®

# Mixed Signal Design Junior Seminar

Blaine Prestwich November 5, 2019

### Who is ON Semiconductor?



ON Semiconductor®

- Spin off of Motorola in 1999 (Standard IC products & Discrete power components) (A) MOTOROLA
- Growth Intrinsic & Acquisition
  - Cherry, Catalyst, PulseCore, CMD, Sound Design





- SANYO Semiconductor
- Cypress Image Sensor, Trusense, Aptina, AXSEM
- Fairchild



- Quantenna Quantenna
- 32,000 employees world wide, \$5.9B Revenue.
- ON Semiconductor today is a Fortune 500 Company and a top 20 Semiconductor non-memory supplier of standard products and custom solutions.

"Driving energy efficient innovations, empowering customers to reduce global energy use"

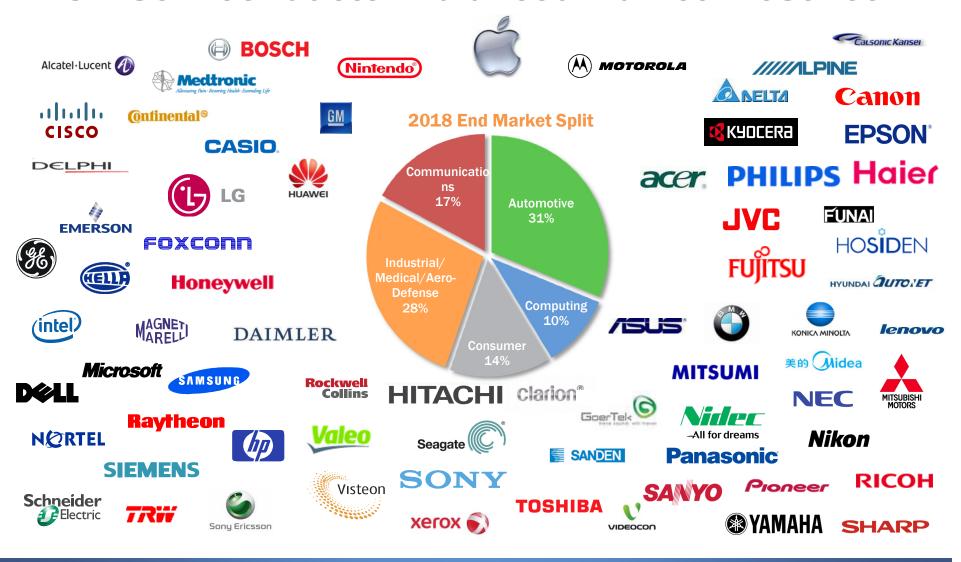
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Cherry
Semiconductor

### **ON Semiconductor Balanced Market Presence**



# Utah Design Center (UTDC) Lindon, UT

- Locations
  - Head Quarters Phoenix, AZ
  - Design Centers Lindon, UT; Pocatello, ID; Philadelphia, PA; Phoenix, AZ;
     Dallas, TX; Bangalore, India; Belgium; Czech Republic; More...
- UTDC comes through AMIS Mixed Signal Design expertise
- UTDC Charter
  - Design World Class Mixed Signal ASICs & ASSPs
  - Provide Compelling Embedded Core Solutions for Mixed Signal Applications
  - Recruit Engineering Talent out of U of U and BYU.
- Ongoing Internship Program
  - Seniors & Graduate Students
  - Fall, Winter, Spring & Summer semesters
- Opportunities for Employment...if interested in MS Design

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### Mixed-Signal Design

- What kind of design do you like to do?
- Is Digital Logic design your favorite?
- Is Embedded Software design your favorite?
- Is Analog Transistor level design your favorite?
- Or do you like them all!



### What is Mixed-Signal Design?

- Mixed Signal Designs include both Analog and Digital circuits on the same Integrated Circuit.
- What are Analog Circuits I/Os, Amplifiers, Oscillators, Comparators, Regulators, References, Temperature Sensors, Data Converters, Level shifters, etc.
- What are Digital Circuits Logic gates, State Machines, Control, Microprocessors with Firmware, Memories, Communication, Programmability, Analog Trim, FPGA's etc.
- Are all Digital IC's Mixed Signal?
- Are all Analog IC's Mixed Signal?

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### Why Mixed-Signal Design?

- The Real World is Analog Colors, Sounds ☼,
   Temperatures
   Textures
   Motion, etc.
- Transducers turn the Analog world into Voltage and Current signals.
- Analog Circuits are the "senses" of the Integrated Circuit they condition the signal and prepare it for interpretation and manipulation.
- Digital Circuits and Software are the "brains" of the Integrated Circuit – they efficiently process the information.
- Analog Circuits create the "action" as they send the interpretation or modified signal back into the real world.
- Analog to Digital (ADC) Converters and Digital to Analog Converters (DAC) link the senses to the brain and the brain to the action.

### **Some Mixed Signal Applications**



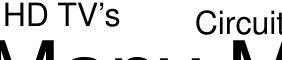




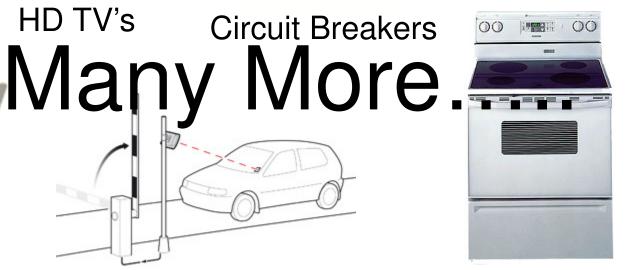


Medical Imaging

Cell Phones



Circuit Breakers







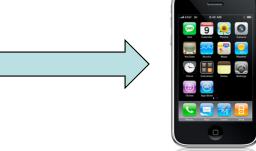
RFID and Automotive

**Appliances** 

## Why Mixed Signal Integrated Circuits?

- As applications become smaller and more complex, there is pressure to save board space and power
  - Cell phones
  - Tablet computers
  - RF ID sensors
  - MP3 players
  - Laptops





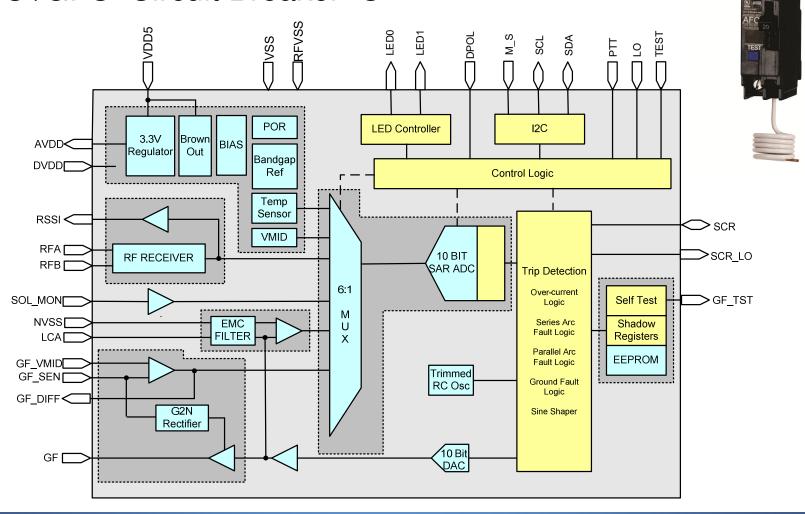


Automotive

applications

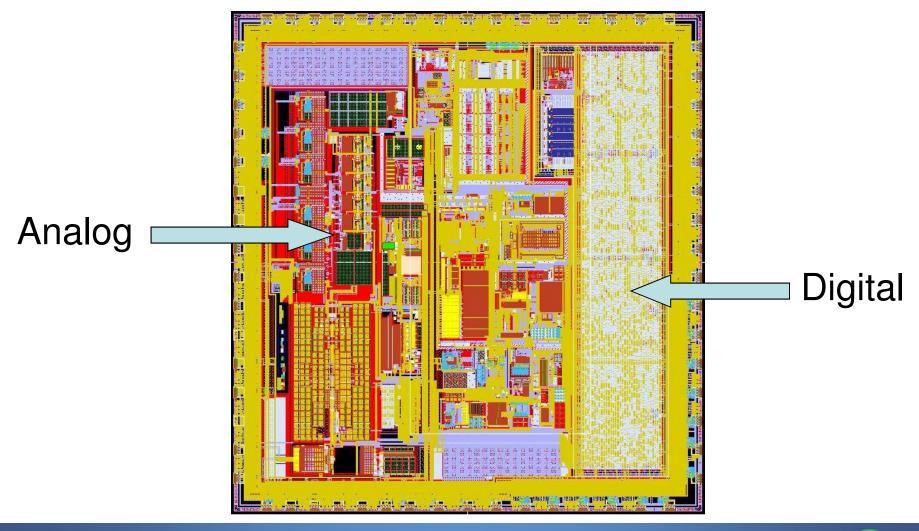
- Industrial control
- Power management
- Audio
- Displays (Flatscreen monitors, touchscreens, HDTV, 3-D TV)

#### AFCI/GFCI Circuit Breaker IC

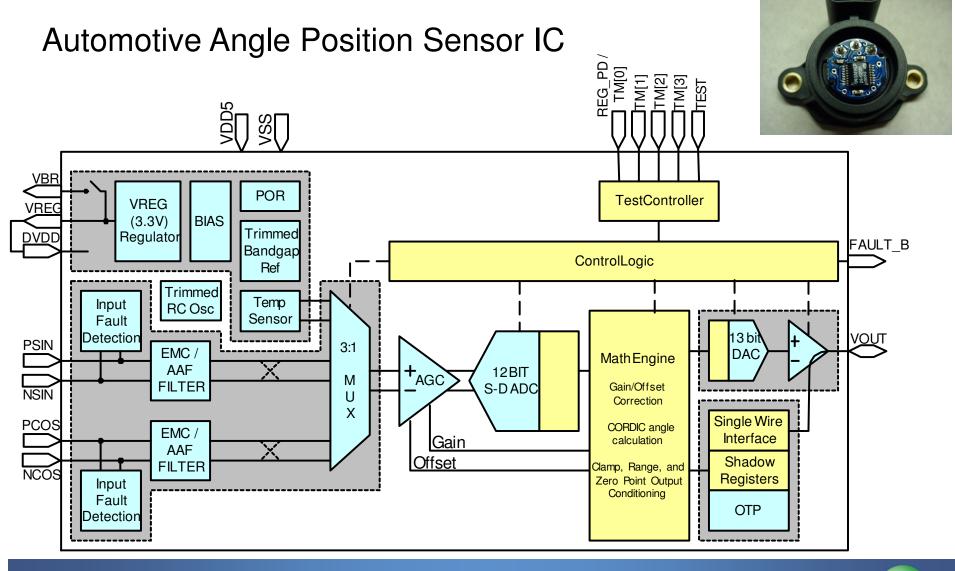


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AFCI/GFCI Circuit Breaker IC

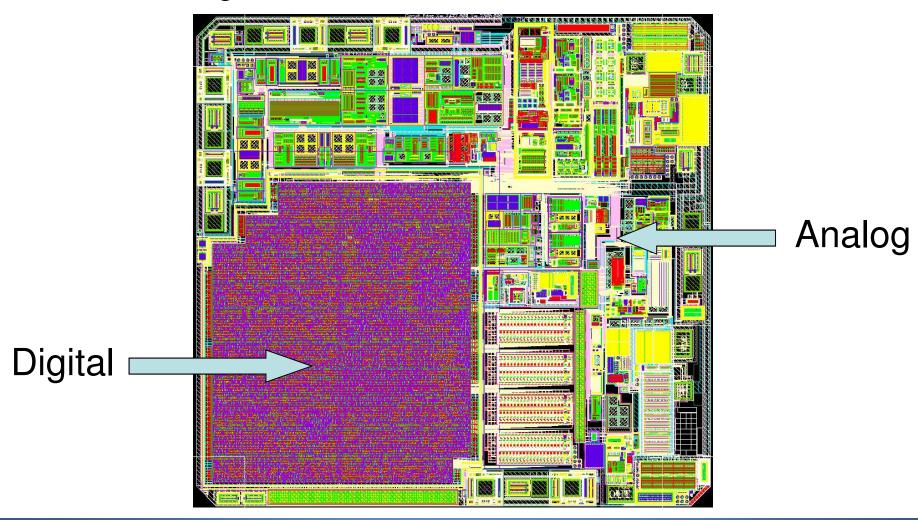


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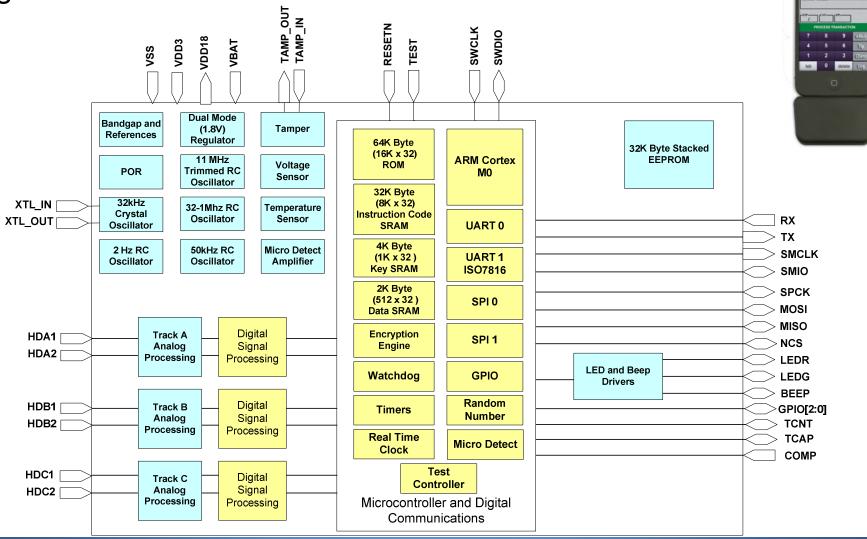


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Automotive Angle Position Sensor IC



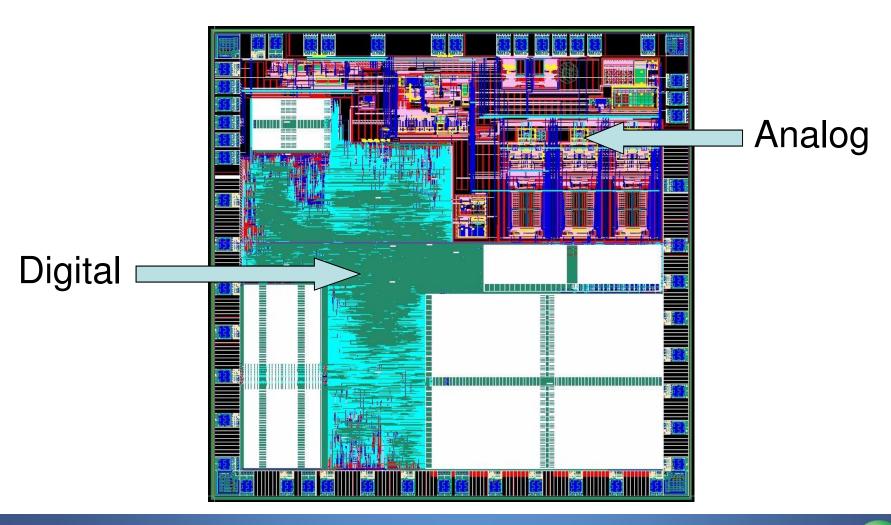
Magnetic Card Reader IC



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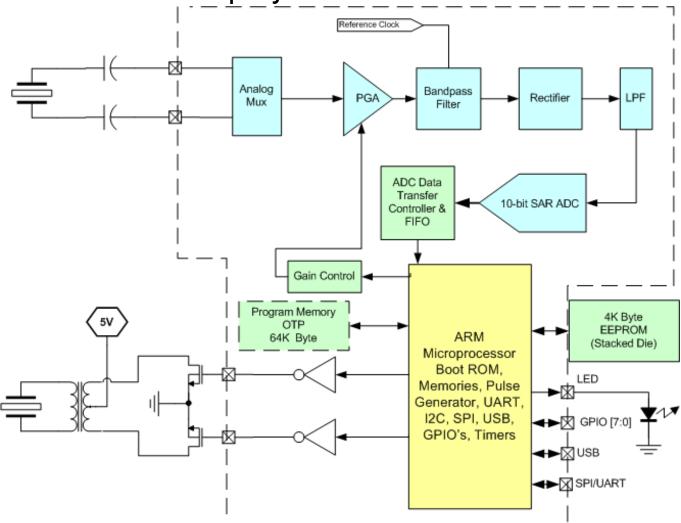
IMAG [

Magnetic Card Reader IC



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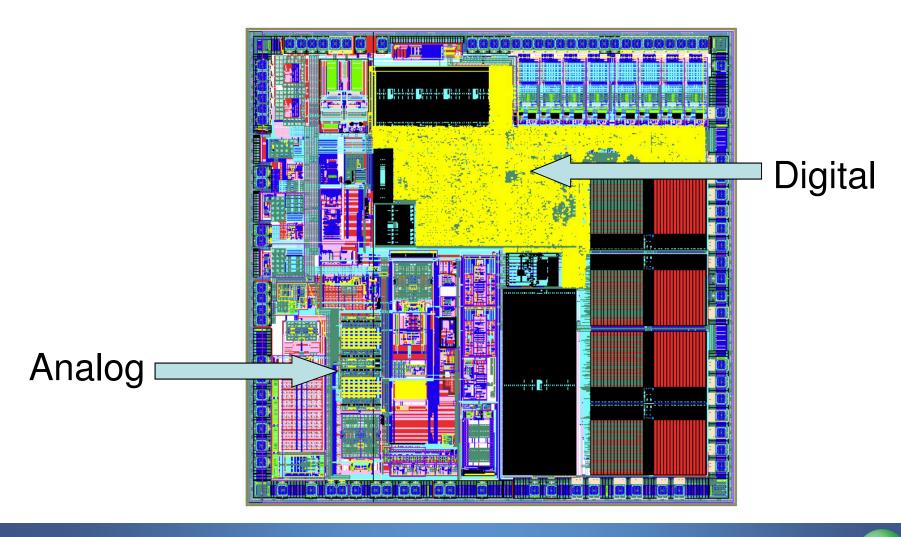
**Touch Screen Display** 





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Touch Screen Display

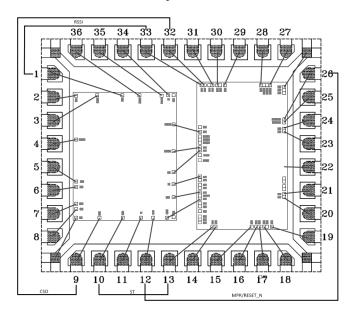


### Multi-Chip Modules – System in a Package

- What do you do when you want to combine different process technologies?
- Multi-chip Modules allow creation of a single packaged solution using two or more die.
  - High speed generic processor
  - Memories (Flash, SRAM, etc.)
  - Custom Analog

Top Die Stacked Die Stacked Die Bottom Die 2.184 x 2.684 mm

#### Side-by-side Die



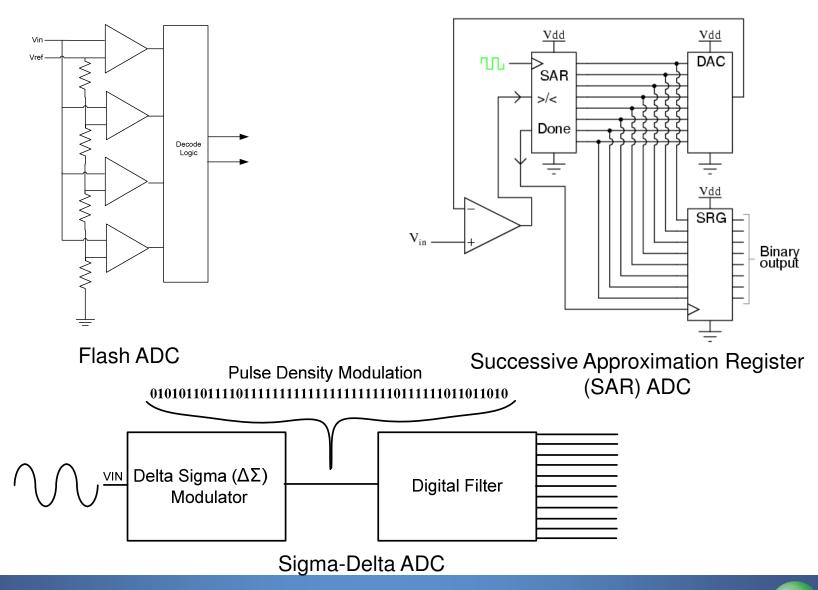
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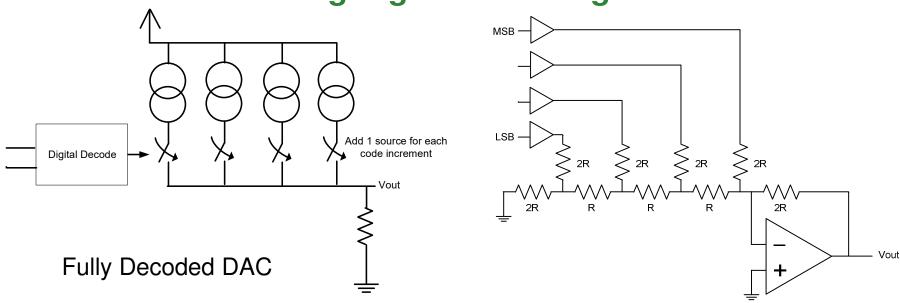
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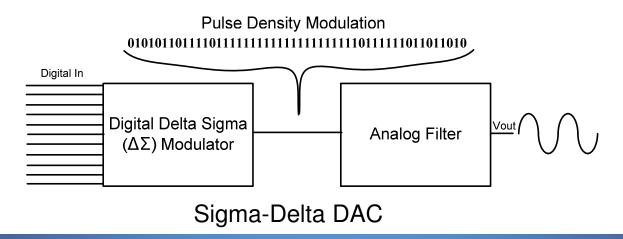
### **Converting Analog to Digital - ADCs**



### **Converting Digital to Analog - DACs**



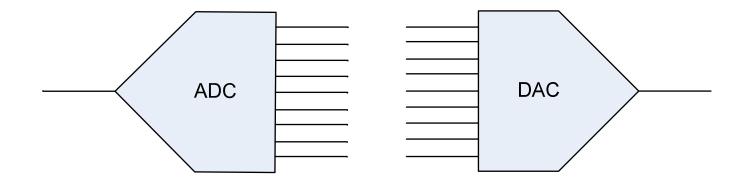
R/2R (Resistor Ladder) DAC



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### Challenge: How to choose the right Data Converter

How do you know which ADC or DAC you should use?



- Questions to ask:
  - How many bits do I need?
  - What is the required conversion rate?
  - How much area on the IC do I have?
  - Which Architecture will best support this application?

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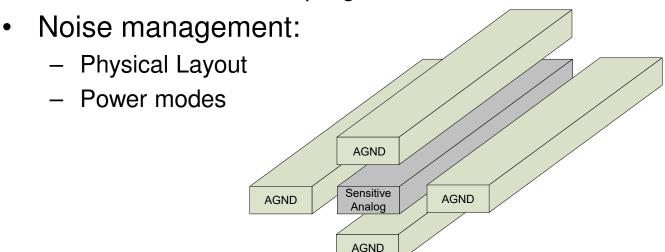
### Challenge: Mixed-Signal Verification

- Verify Analog, Digital, and Firmware Independently (Good).
- Capture Analog to Digital & Digital to Analog Interfaces and play back in the independent simulations (Better)
- Mixed-mode Simulations with Analog, Digital, and Firmware in one simulation environment. (Best)
  - Combines both a transistor level and RTL level simulator: ex: Cadence Spectre/Verilog or AMS simulators.
- Simulate firmware running on Embedded Core to test the mixed-signal IC.
- FPGA emulation with generic analog interface

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### Challenge: Mixed-Signal Noise Management

- Many Possible Electrical Noise Sources in Mixed Signal Design.
- Which is most sensitive to noise Analog or Digital?
- Noise Sources:
  - Power supply noise coupling
  - Substrate noise coupling
  - Line to line noise coupling



Noise Management Example: Silicon Coaxial Shielding

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### Challenge: Mixed-Signal IC Testing

- Every part of the IC design must be tested.
- Design for Test (DFT) planning needs to include test modes to be able to access direct inputs and outputs of Analog circuits.
- DFT planning needs to include a digital test mode using serial scan-chains and ATPG (Automatic Test Pattern Generation).
- BIST (Built-In-Self-Test) can be an efficient way to test both analog and digital circuits.
- Embedded Cores can be used to facilitate IC Testing.
  - Exercise Analog and Digital Peripherals
  - Enable Low Power Modes

### Why Become a Mixed Signal IC Designer?

- Be a true designer
- Find new ways to solve the worlds problems
- Large variety of applications, industries, and technologies
- High demand skill set (portable to other industries)
- Work with diverse teams of engineers, customers, and professionals
- Opportunities for patents and publications
- Fun, challenging, diverse work experience
- \$\$\$\$

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### How to become a Mixed Signal Designer

- Complete a variety of courses in both Analog and Digital Integrated Circuit design techniques as well as Computer Architecture, Embedded Software, IC Testing and Verification, Digital Signal Processing (DSP), Communications, RF, etc.
- Mixed Signal Designers generally have some areas of expertise on different facets of Analog, Digital, and Software design with a good understanding of the other areas.

### How to become a Mixed Signal Designer

- Core Courses
  - 2260 Fund Electric Circuits
  - 2280 Fund Eng Electronics
  - 1410/2000 Computer Programming
  - 3700 Digital Systems Design
  - 3110 Engineering Electronics II
  - 3510 Introduction to Feedback Systems
  - 5710 Digital VLSI Design
  - 5720 Analog Integrated Circuit Design
- Mixed-Signal Circuit Design Senior Project
- Other Helpful Courses
  - 3710 Computer Design Laboratory
  - 3810 Computer Organization
  - 5530, 5580 Digital Signal Processing
  - 5201, 5202 Semiconductor Physics
  - 5740 Computer Aided Design of Digital Circuits
  - 5745 Testing and Verification of Digital Circuits
  - 5780 Embedded System Design
  - 5830 VLSI Architecture

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- 6770 Advanced Digital VLSI System Design
- 6721 Analog Integrated Circuits Lab



### **Ethics In Engineering**

 In 2016, 2017, 2018, and 2019 ON Semiconductor was recognized as one of the Worlds' Most Ethical Companies.









- What does Ethics have to do with Engineering?
- Ethics and Engineering are highly related in the areas of Safety, Security, and Sustainability.

### Safety in Engineering

- Engineers must work to keep their customer's and the general public safe.
  - Bridge disasters in late 1800s and early 1900s
  - Automotive Brakes and Airbag recalls
  - Medical Implants and Diagnostic equipment



 Lapses in safety are very costly to a companies reputation and bottom line.

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### **Security in Engineering**



- Engineers must also be aware of security for their company and customers
  - Secure information is being stolen everyday!
- Engineering is about creating and using ideas and information
- Ideas and information are valuable assets called Intellectual Property (IP)
  - Engineers need to protect their company/customers/partners IP
  - Engineers must avoid "stealing" IP that isn't theirs or their customers
    - Re-use of your own IP is a great idea, however it is unethical to use IP that is not yours (from the internet, conference paper, peers, previous employer).

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# Sustainability in Engineering

- Engineers must understand their impact on the world around us
  - Engineering builds with limited resources
    - Hard materials
    - Energy
    - Manpower
  - Engineering can lead to pollution
    - Engineering may use hazardous materials
    - Volkswagon "diesel dupe"
  - Engineers must help create and follow sustainability guidelines
- We owe it to future generations to work to improve the efficiency and sustainability of our efforts.



### **Ethics In Engineering**

- Engineers manage technical activities
  - Sometimes tough decisions need to be made regarding doing it right or doing it on-time. The goal is to do both.
  - Products can always be better engineers decide when is it good enough.
  - Cutting corners always has a cost
    - Supporting data must be sound and have integrity.
- Sometimes the ethical decision is not clear, but engineers have the tools needed to make the right and ethical choice.

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# Mixed Signal Design Questions?

• Questions?



### References

- ON Semiconductor Utah Design Center, Engineers & Designs
- The Fundamentals of Mixed Signal Testing, Nov 2007 (Soft Test, Inc.)
- All About Circuits (<u>www.allaboutcircuits.com</u>), 1999-2000
- CMOS Circuit Design, Layout, and Simulation, 1998 (IEEE PRESS)

